

RESPONSE UNDER 37 C.F.R. §1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2811

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Douglas D. Coolbaugh, et al.

Examiner: Cuong Q. Nguyen

Serial No: 09/516,615

Art Unit: 2811

Filed: March 1, 2000

Docket: BUR919990190US1 (13020)

For: METHOD OF FABRICATING A  
POLYSILICON CAPACITOR UTIL-  
IZING FET AND BIPOLAR BASE  
POLYSILICON LAYERS

Dated: July 26, 2002

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Assistant Commissioner for Patents  
United States Patent and Trademark Office  
Washington, D.C. 20231

RESPONSE UNDER 37 C.F.R. §1.116

Sir:

In response to the Final Rejection dated May 28, 2002, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

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IN THE CLAIMS:

JUL 26 2002

Please amend Claims 24, 26 and 30 to read as follows:

TECHNOLOGY CENTER 2800

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: July 26, 2002

*Gina Johnston*  
Gina Johnston